

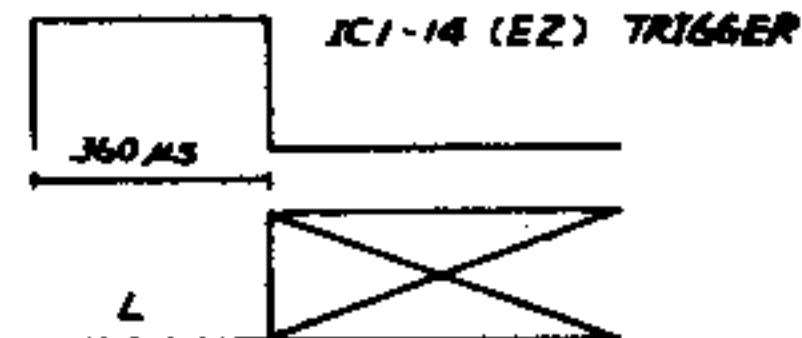
PLL

26 IC1 D/A DATA OUT

FREQUENCY	PIN	2	3	4	5	6	7	8	9	10	11
	C0	C1	C2	C3	D0	D1	D2	D3			
0.00 kHz	L	L	L	L	L	L	L	L	L	L	L
0.01	L	L	L	L	H	L	L	L			
0.02	L	L	L	L	L	H	L	L			
0.03	L	L	L	L	H	H	L	L			
0.04	L	L	L	L	L	L	H	L			
0.05	L	L	L	L	L	H	L	H			
0.06	L	L	L	L	L	H	H	H			
0.07	L	L	L	L	L	H	H	H			
0.08	L	L	L	L	L	L	H	H			
0.09	L	L	L	L	H	L	L	H			
0.10	H	L	L	L	L	L	L	L			
0.11	H	L	L	L	L	H	L	L			
0.12	H	L	L	L	L	H	H	L			
0.13	H	L	L	L	H	H	H	L			
0.95	H	L	L	H	L	L	H	L			
0.96	H	L	L	H	H	H	H	L			
0.97	H	L	L	H	L	H	H	L			
0.98	H	L	L	H	H	L	L	H			
0.99	H	L	L	H	L	L	L	H			
1.00	L	L	L	L	L	L	L	L			
0.0 kHz	L	L	L	L							
0.1	H	L	L	L							
0.2	L	H	L	L							
0.3	H	H	L	L							
0.4	L	L	H	L							
0.5	H	L	H	H							
0.6	L	H	H	L							
0.7	H	H	H	L							
0.8	L	L	L	H							
0.9	H	L	L	L							
1.0	L	L	L	L							

27 VFO. MEMORY

IC1. PIN	33	34	35	36
VFO	A0	A1	A2	A3
A	L			
B	H			
MEMORY		L		
ON		H		
NDR			L	
SPT			H	
WRITE	OFF			
ON			H	

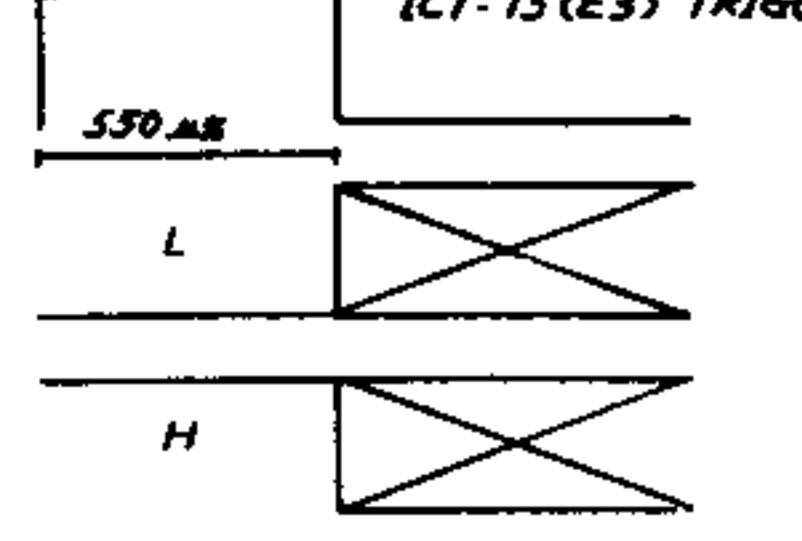


28 MODE SW

BAND	MODE	J4	J4	J4	JC7	JC7	JC7	JC7
		-2	-3	-4	-8	-9	-13	-10
3.5 MHz	CW	L	L	L	L	L	L	L
~7 MHz	LSB	H	L	H	H	L	H	L
USB	H	H	L	L	L	H	L	H
10 MHz	CW	L	L	L	L	L	L	L
~28 MHz	LSB	H	L	H	H	L	L	H
USB	H	H	L	L	H	H	H	L

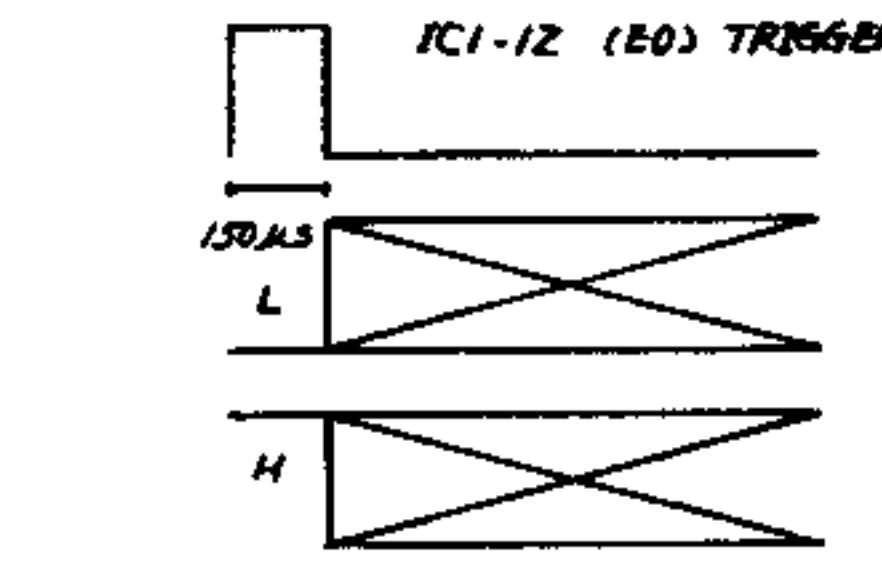
29 VFO PITCH

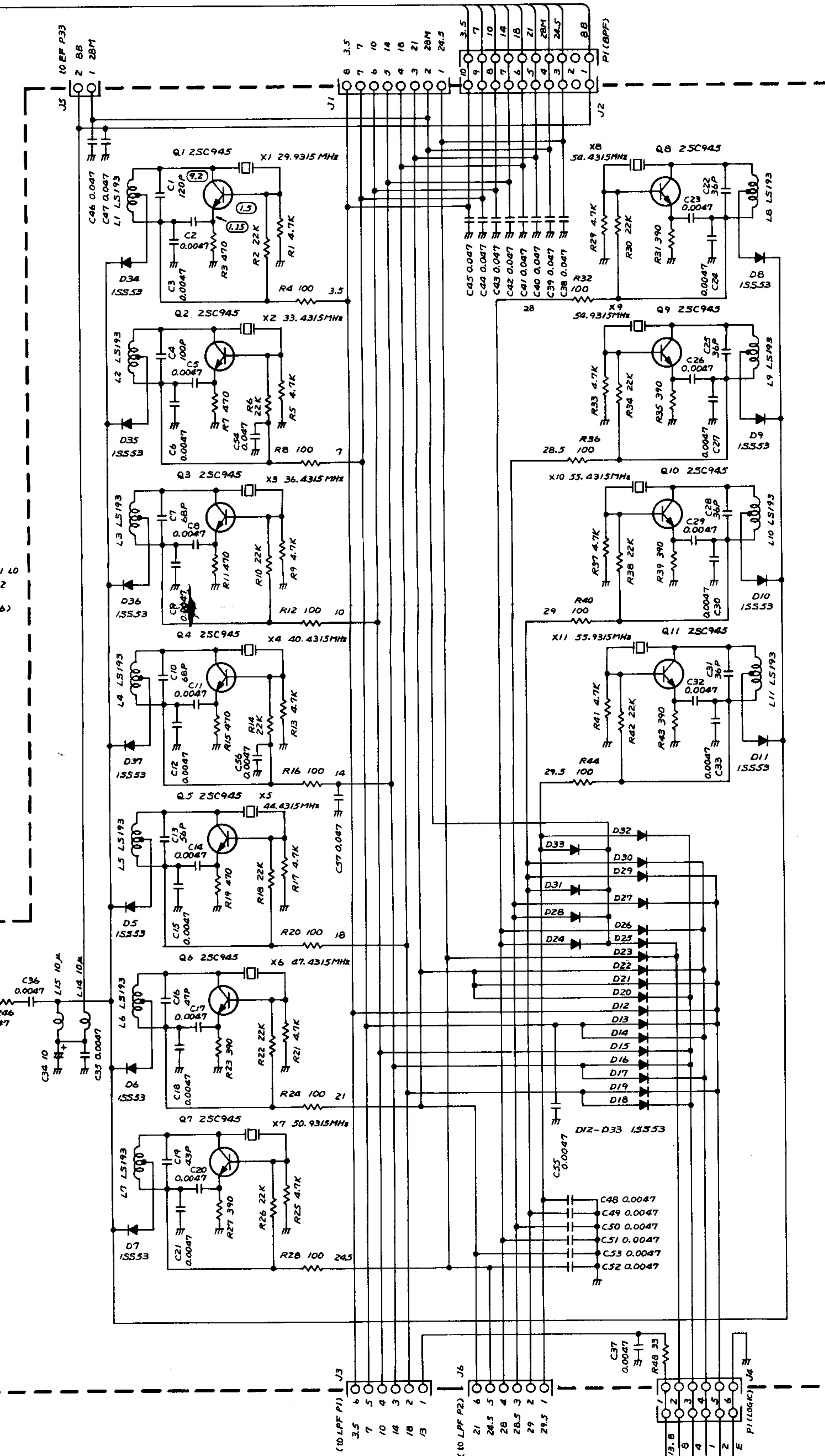
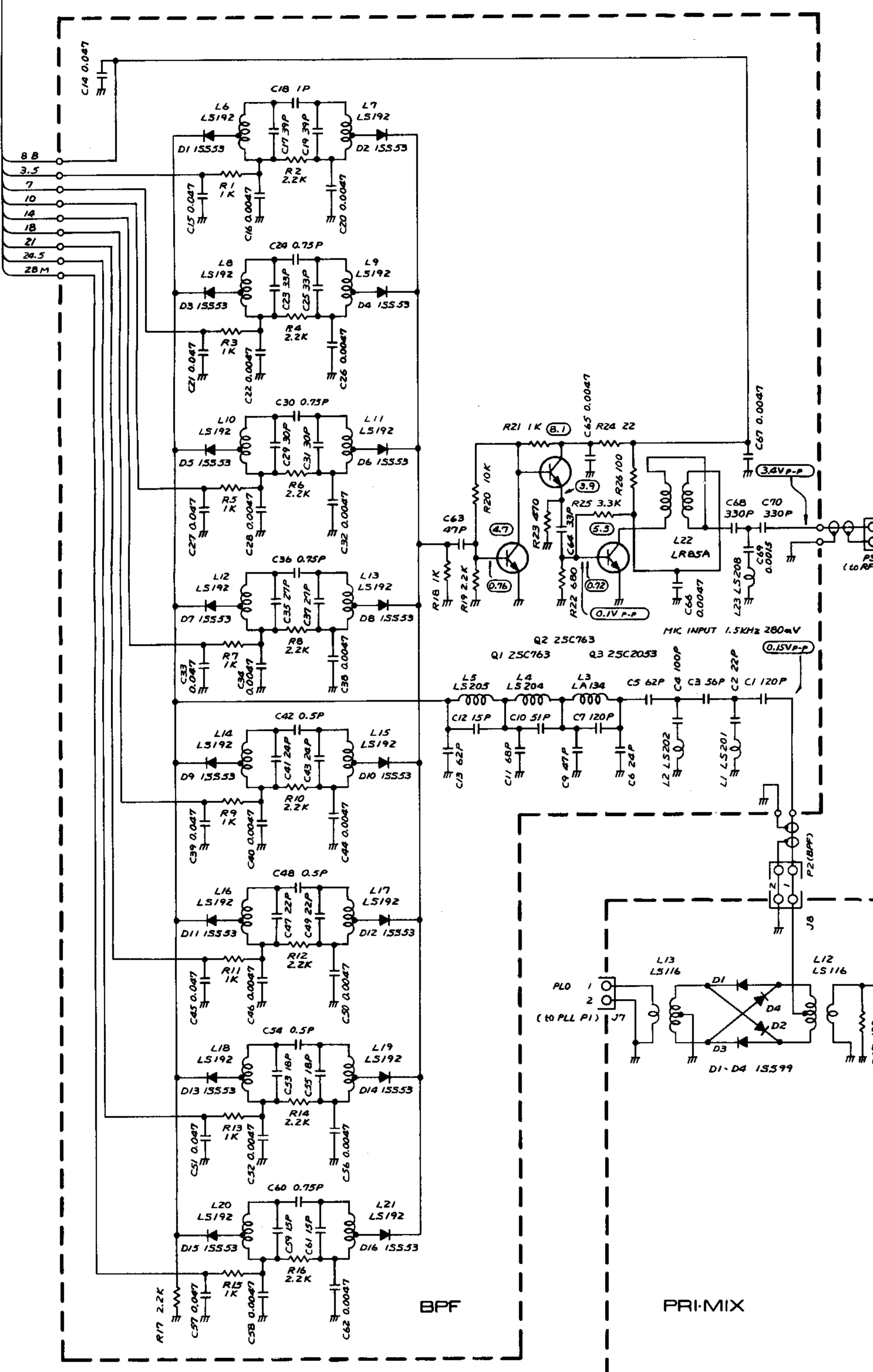
IC1. PIN	33	34	35	36
VFO	A0	A1	A2	A3
10Hz	H	L	L	H
1K	L	L	L	H
POWER	OFF			
ON			L	H



30 BAND SW

BAND	IC6	IC6	IC6	IC6
	9-8	11-10	1-2	3-4
3.5 MHz	L	H	L	L
7	H	H	L	L
10	L	L	H	L
14	H	L	H	L
18	L	H	H	L
21	H	H	H	L
24.5	L	L	L	H
28	H	L	L	H
28.5	L	H	L	H
29	H	L	H	H
29.5	L	L	H	H
NC	L	L	L	L





# IC-730(S) SCHEMATIC DIAGRAM

1 MIC(+8V)  
2 +8V  
3 VFO  
4 PTT  
5 GND  
6 NF-GND  
7  
8

RF-POWER

MIC-GAIN

RIT

RF-GAIN

IF-SHIFT

AF-GAIN

TRANSMIT  
VOX  
NB  
AGC-F  
PRE-AMP  
WRITE  
MEMO

LOCK  
10  
100  
1K

NOR/SPT  
VFO-A/B

MODE  
AM  
USB  
LSB  
CW-N  
CW

POWER

